Registration Form:

1. Name:

2.Designation with Department:

3. Institution:

4. Address for Correspondence:

5. Mobile no:

6. Email ID:

7. Qualification with Specification:

8. Experience (in years):

9. Accomodation Required (Yes/No):
Food preference(Veg/ Non Veg):

10. Payment Details:

DD no:
Date:
Bank:

Date: Signature of the Applicant

Sponsorship Certificate:

Dr./Mr./Ms..... is an employee of our Institute/Organization and is hereby sponsored to participate in the FDP on Semiconductor Devices from Micro - Nano,if he/she is selected.

Date Signature of Head of Institution
(Seal)

Important Dates

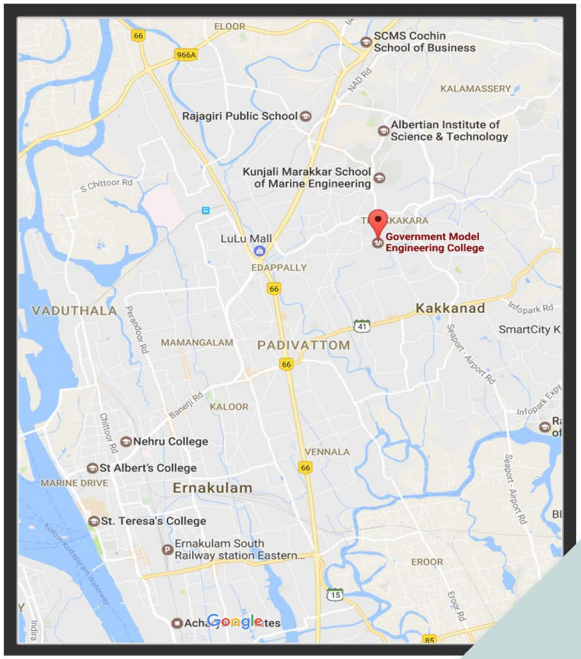
Last date of online registration	: 14/01/2017
Intimation of selection by email	: 16/01/2017
Last date of receipt of application	: 23/01/2017

How to reach:

By Road: Ernakulam City -> Edapally -> Purutheli Jn -> Temple Jn -> MEC.

By Rail: From both Ernakulam North , Ernakulam South or Alwaye Railway Station , reach Edapally. Then follow the path mentioned above.

By Air: From Cochin Airport -> Edapally same as above.Else, airport -> HMT Jn -> Seaport -Airport road -> MEC.



Faculty Development Programme on SEMICONDUCTOR DEVICES: FROM MICRO TO NANO

(30th January – 3rd February 2017)

Academic Coordinator

Dr. Jacob Thomas V
Principal,
College of Engineering,
Kallooppara

Course Coordinators

Dr.Jobymol Jacob
Mr.Sajeesh M

Institute of Human Resources Development
Thiruvananthapuram
Established by Govt. of Kerala

About the Programme:

As the semiconductor device dimension shrinks from micro to nano scale, researchers face a lot of challenges in fabrication as well as modeling of new device structures. The faculty development programme on "Semiconductor Devices: From Micro to Nano" introduces the fundamental concepts behind device modelling and fabrication technology with a highlight on the issues and challenges faced while moving from micro to nanoscale devices. This programme explores the new research areas and directs the audience towards the research challenges in these areas.

Course Contents:

> Semiconductor Device Fabrication Tech nology
> CMOS Technology: Future Challenges
> Compound Semiconductor: Technology & Challenges
> MEMS/NEMS Technology
> Modeling Semiconductor Devices: Basic Concepts
> Compact modeling
> Fundamentals of Nanoelectronics
> Modeling Nanoscale Devices
> Modeling High Speed Devices
> Silicon Photonics
> Device Modeling Lab/ Field Visit

Resource Persons:

> Prof. Nandita DasGupta, IIT Madras
> Prof. Enakshi Bhattacharya, IIT Madras
> Prof. Shreepad Karmalkar, IIT Madras
> Prof. Amitava DasGupta, IIT Madras
> Dr. Bijoy KrishnaDas, IIT Madras
> Dr. Deleep R. Nair, IIT Madras
> Dr. Arvind Ajoy, IIT Palakkad
> Dr. Sreenidhi T., Siddaganga Institute of Technology, Tumkur
> Dr. Jobymol Jacob, Model Engineering College Thrikkakara

Course Fee: ₹ 35,000/- per participant.

How To Apply:

Faculty members from AICTE approved Engineering Colleges can apply online. Website: http://www.mec.ac.in/mec/fdp/fdpsdmn.php. Selected candidates will be intimated through email. Registration form in the prescribed format duly filled in, along with non-refundable DD drawn in favour of The Academic Coordinator, A/c No. 67359043802, IHRD FDP, payable at SBT Kallooppara (IFSC code SBTR0000464), is to be sent to the following address.

The Academic Coordinator,
FDP on "Semiconductor Devices: From Micro to Nano" ,
Institute of Human Resources Development, Vazhuthacaud,
Thiruvanathapuram – Pin. 695014

A sponsorship certificate from the institution is mandatory. The last date of receipt of applications along with the required fee is January 23, 2017. It is also mandatory to send scanned copy of the application and demand draft through e-mail to fpd@ceconline.edu and a copy to fdpsdmn@gmail.com.

Accomodation:

IHRD will arrange food and accommodation for the participants, based on prior requests No TA will be paid for the participants.

Venue:

Govt. Model Engg College.
Website: www.mec.ac.in
Call: +91-484-2577379

About IHRD:

The state of Kerala has ever been a model to the whole countrynot only for its highlevel of standardsin overall human development but also for its sparkling performance in technical education in Kerala. Estd in 1987 as the state's first self-financing education institution, the purpose and aimof such an institution was to improve the technical standards in the state on a cost-sharing basis;between the public and the state govt. The pioneer catalyst in the ever improving technical education in the state, IHRD has now under its wings 15 technical higher secondary schools, 8 polytechnic colleges, 44 colleges of applied sciences and 9 engineering colleges.

Contact information:

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